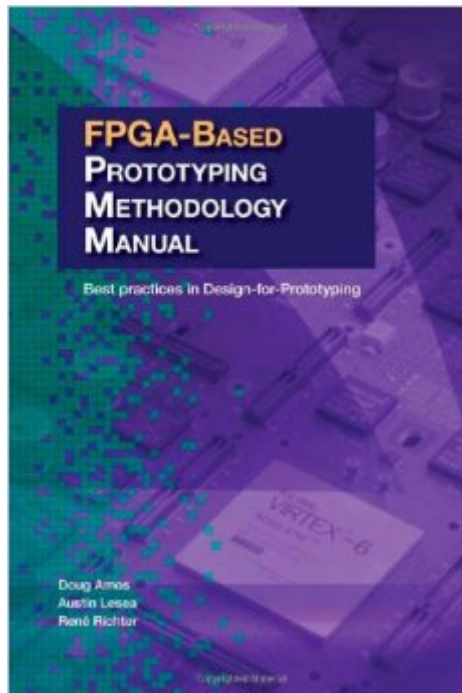


The book was found

FPGA-Based Prototyping Methodology Manual: Best Practices In Design-For-Prototyping



Synopsis

This book collects the best practices FPGA-based Prototyping of SoC and ASIC devices into one place for the first time, drawing upon not only the authors' own knowledge but also from leading practitioners worldwide in order to present a snapshot of best practices today and possibilities for the future. The book is organized into chapters which appear in the same order as the tasks and decisions which are performed during an FPGA-based prototyping project. We start by analyzing the challenges and benefits of FPGA-based Prototyping and how they compare to other prototyping methods. We present the current state of the available FPGA technology and tools and how to get started on a project. The FPMM also compares between home-made and outsourced FPGA platforms and how to analyze which will best meet the needs of a given project. The central chapters deal with implementing an SoC design in FPGA technology including clocking, conversion of memory, partitioning, multiplexing and handling IP amongst many other subjects. The important subject of bringing up the design on the FPGA boards is covered next, including the introduction of the real design into the board, running embedded software upon it in and debugging and iterating in a lab environment. Finally we explore how the FPGA-based Prototype can be linked into other verification methodologies, including RTL simulation and virtual models in SystemC. Along the way, the reader will discover that an adoption of FPGA-based Prototyping from the beginning of a project, and an approach we call Design-for-Prototyping, will greatly increase the success of the prototype and the whole SoC project, especially the embedded software portion. Design-for-Prototyping is introduced and explained and promoted as a manifesto for better SoC design. Readers can approach the subjects from a number of directions. Some will be experienced with many of the tasks involved in FPGA-based Prototyping but are looking for new insights and ideas; others will be relatively new to the subject but experienced in other verification methodologies; still others may be project leaders who need to understand if and how the benefits of FPGA-based prototyping apply to their next SoC project. We have tried to make each subject chapter relatively standalone, or where necessary, make numerous forward and backward references between subjects, and provide recaps of certain key subjects. We hope you like the book and we look forward to seeing you on the FPMM on-line community soon (go to www.synopsys.com/fpmm).

Book Information

Paperback: 494 pages

Publisher: Synopsys Press (March 2, 2011)

Language: English

ISBN-10: 1617300047

ISBN-13: 978-1617300042

Product Dimensions: 6.1 x 1 x 9.2 inches

Shipping Weight: 1.4 pounds (View shipping rates and policies)

Average Customer Review: 5.0 out of 5 stars [See all reviews](#) (1 customer review)

Best Sellers Rank: #2,278,329 in Books (See Top 100 in Books) #64 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Logic](#) #362 in [Books > Computers & Technology > Programming > Software Design, Testing & Engineering > Logic](#) #707 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design](#)

Customer Reviews

Material is well covered. A bit dated but very useful for someone wanting to learn about FPGA and its application in prototyping. Book was sent within a god time frame.

[Download to continue reading...](#)

FPGA-Based Prototyping Methodology Manual: Best Practices in Design-For-Prototyping FPGA Prototyping By Verilog Examples: Xilinx Spartan-3 Version Percutaneous Absorption: Drugs--Cosmetics--Mechanisms--Methodology: Drugs--Cosmetics--Mechanisms--Methodology, Third Edition, (Drugs and the Pharmaceutical Sciences) Microsoft® Exchange Server 2010 Best Practices (IT Best Practices - Microsoft Press) Design of Softcore DSP Processors on FPGA Chips Advanced Digital Logic Design Using VHDL, State Machines, and Synthesis for FPGA's RESTful API Design: Best Practices in API Design with REST (API-University Series Book 3) Digital Systems Design and Prototyping: Using Field Programmable Logic and Hardware Description Languages Practical FPGA Programming in C Advanced Mathematics for FPGA and DSP Programmers Advanced Mathematics for FPGA and DSP Programmers: Conquering Fixed-Point Pitfalls 100 Power Tips for FPGA Designers Le Escena Presente/ The Present Scene: Teoria y metodologia del diseno escenografico / Theory and Methodology of Stage Design (Coleccion Artes Visuales / Visual Arts Collection) (Spanish Edition) Home Based Business Escape Plan: How To Make \$10,000 Per Month With Your Own Part-Time, Online Lifestyle Business: Home Based Business Ideas (Home Based Business Opportunities) Pediatric Nursing Care: Best Evidence-Based Practices Best of the Best from Hawaii Cookbook: Selected Recipes from Hawaii's Favorite Cookbooks (Best of the Best State Cookbooks) Best of the Best from New Mexico Cookbook: Selected Recipes from New Mexico's Favorite Cookbooks (Best of the Best Cookbook) Best Man Best Speech: How to Be the

Best Best Man Jokes : Best Jokes 2016: (Funny books, Joke books, Funny jokes, Best jokes 2016, Best jokes 2015) (Best Jokes 2016 Bundle) BEST BOOKS: Recommended Reading-Best American Literature (Fiction & Nonfiction), Must Read Titles in English, Best Young Adult Books, the Best Kindle Books, ... Novels & Book Club Picks (Good Reads 1)

[Dmca](#)